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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/775,900	02/10/2004	Vincent Alan Larsen	24089-09285	1196
758	7590	07/10/2007		
FENWICK & WEST LLP SILICON VALLEY CENTER 801 CALIFORNIA STREET MOUNTAIN VIEW, CA 94041			EXAMINER YOUNG, NICOLE M	
			ART UNIT 2139	PAPER NUMBER
			MAIL DATE 07/10/2007	DELIVERY MODE PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

## Office Action Summary

Application No.

10/775,900

Applicant(s)

LARSEN, VINCENT ALAN

Examiner

Nicole M. Young

Art Unit

2139

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 10 February 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 09 July 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date See Continuation Sheet.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_.

### DETAILED ACTION

This communication is in respond to the application filed on February 10, 2004. Claims 1-20 are pending.

#### ***Claim Rejections - 35 USC § 112***

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

**Claim 3** recites the limitation "memory structure". There is insufficient antecedent basis for this limitation in the claim.

#### ***Claim Rejections - 35 USC § 102***

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

**Claims 1-20** are rejected under 35 U.S.C. 102(e) as being anticipated by **Wilson (US 6,941,456)**.

**Claims 1, 9, and 14** disclose a memory structure storing instructions for a method of providing access for a user to resources through a process, said method comprising the steps of:

receiving user identification information (Fig. 10 steps 250 and 252, wherein the metadata and ID are the identification information, and associated text in column 7);

identifying user resource access information associated (Figure 10 step 252 permissions, and associated text in column 7) with the user identification information, wherein the user resource access information includes process resource access

information associated with a process (Fig. 10 wherein the public key would be the process resource access information which is associated with the process of decrypting the data for access, and associated text in column 7) ;

determining when an executing process attempts to accesses a specified resource (Figure 10 steps 254, 258, and 254 determine if the process is accessing the specified resource, and associated text in column 7);

checking the process resource access information associated with the process when the process attempts to access the specified resource to determine if access to the specified resource by the process is permitted ((Figure 10 steps 254, 258, and 254 determine if the process is accessing the specified resource, and associated text in column 7);

allowing the process to access the specified resource if access permission is indicated (Figure 10 step 266 returns the file, and associated text in column 7); and

denying the process access to the specified resource if access permission is not indicated (Figure 10 step 256 returns an access denied error, and associated text in column 7).

**Claims 2 and 15** disclose the memory structure of claim 1, wherein said memory structure is an integrated circuit (column 8 lines 51-67, integrated circuit chip).

**Claims 3 and 10** disclose the memory structure of claim 2, wherein said memory structure is a read-only memory integrated circuit (column 8 lines 51-67, ROM).

Art Unit: 2139

**Claims 4 and 11** disclose the memory structure of claim 3, wherein said read-only memory integrated circuit is a programmable read-only memory integrated circuit (column 8 lines 51-67, PROM).

**Claim 5** discloses the memory structure of claim 1, wherein said memory structure is a flash memory structure (column 3 lines 51-62, flash card).

**Claims 6 and 16** disclose the memory structure of claim 1, wherein said memory structure is an optical storage disk (column 3 lines 51-62, optical disk).

**Claims 7 and 17** disclose the memory structure of claim 1, wherein said memory structure is a magnetic storage disk (column 3 lines 51-62, magnetic disk).

**Claims 8 and 20** disclose the memory structure of claim 1, further comprising an extended instruction set (column 8 lines 51-67, wherein the extended instruction set is the encrypting of data, also column 7 lines 35-40 wherein the extended instruction set is caching the data).

**Claim 12** discloses the integrated circuit of claim 11, wherein said programmable read-only memory is an erasable programmable read-only memory (column 8 lines 51-67, EEPROM).

**Claim 13** discloses the integrated circuit of claim 12, wherein said erasable programmable read-only memory is an electrically erasable programmable read-only memory (column 8 lines 51-67, EEPROM).

**Claim 18** discloses the storage medium of claim 17, wherein said rotating magnetic storage medium is a floppy disk (column 3 lines 51-62, floppy disk).

**Claim 19** discloses the storage medium of claim 16, wherein said rotating magnetic storage medium is a compact disk (column 3 lines 51-62, CD-ROM).

**Note:** Examiner has pointed out particular references contained in the prior arts of record and in the body of this action for the convenience of the applicant. Although the specified citations are representative of the teachings in the art and are applied to the specific limitations within the individual claim, other passages and figures may apply as well. Applicant should consider the entire prior art as applicable to the limitations of the claims. It is respectfully requested from the applicant, in preparing for response, to consider fully the entire reference as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the prior arts or disclosed by the Examiner.

### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nicole M. Young whose telephone number is 571-270-1382. The examiner can normally be reached on Monday through Friday, alt Fri off, 8:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh can be reached on 571-272-3795. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2139

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

NMY  
6/28/2007

  
TAGHI ARANI  
PRIMARY EXAMINER  
7/5/07

Continuation of Attachment(s) 3). Information Disclosure Statement(s) (PTO/SB/08), Paper No(s)/Mail Date :10/04/2004, 6/22/2005, 12/27/2005, 3/30/2006, 8/18/2006, 11/20/2006.